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Robin E. Barnum <i>Rob E. Barnum</i>	(Depositor's name)
	(Signature)
February 24, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/932,381	08/17/2001	Serge Lasserre	TI-31354	4434

TITLE OF INVENTION: LOCAL MEMORY WITH INDICATOR BITS TO SUPPORT CONCURRENT DMA AND CPU ACCESS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/11/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
BRAGDON, REGINALD GLENWOOD	2188		710-022000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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2. For printing on the patent front page, list
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(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
- 1 Robert D. Marshall, Jr.
2 W. James Brady, III
3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Texas Instruments Incorporated

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Robert D. Marshall Jr.

Date February 24, 2005

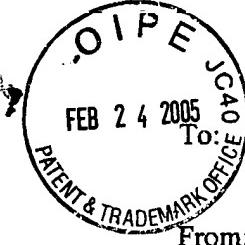
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Robert D. Marshall, Jr.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of TI-31354
Serge Lasserre Art Unit: 2188
Serial No.: 09/932,381 Examiner: Reginald G. Bragdon
Filed: August 17, 2001 Conf. No.: 4434
For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

CERTIFICATION OF FACSIMILE TRANSMISSION

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NAME OF INVENTOR(S): Serge Lasserre	
TITLE OF INVENTION: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access	
TI FILE NO.: TI-31354	DEPOSIT ACCT. NO.: 20-0668
FAXED: 2/24/05	DUE: 5/11/05
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